



RESPONSE UNDER 37 C.F.R. 1.116-  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2877

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JUN 11 2004

Appl. No. : 10/006,398  
Applicants: : Colin D. Yates, Nicholas F. Pasch, and Nicholas K. Eib  
Filed: : November 30, 2001

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Title : ALIGNMENT PROCESS FOR INTEGRATED CIRCUIT STRUCTURES  
ON SEMICONDUCTOR SUBSTRATE USING SCATTEROMETRY  
MEASUREMENTS OF LATENT IMAGES IN SPACED APART TEST  
FIELDSON SUBSTRATE

Grp./ A.U. : 2877  
Examiner : Layla G. Lauchman  
Docket No. : 01-234

CERTIFICATE OF MAILING

I hereby certify that this amendment is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below:

on June 9, 2004  
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John P. Taylor, Reg. No. 22,369

John P. Taylor  
Signature

June 9, 2004  
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RESPONSE TO FINAL REJECTION

Honorable Commissioner for Patents  
Post Office Box 1450  
Alexandria, VA 22313-1450

Date: June 9, 2004

Sir:

This is in response to the Final Rejection mailed April 23, 2004.